

FIG. 1A

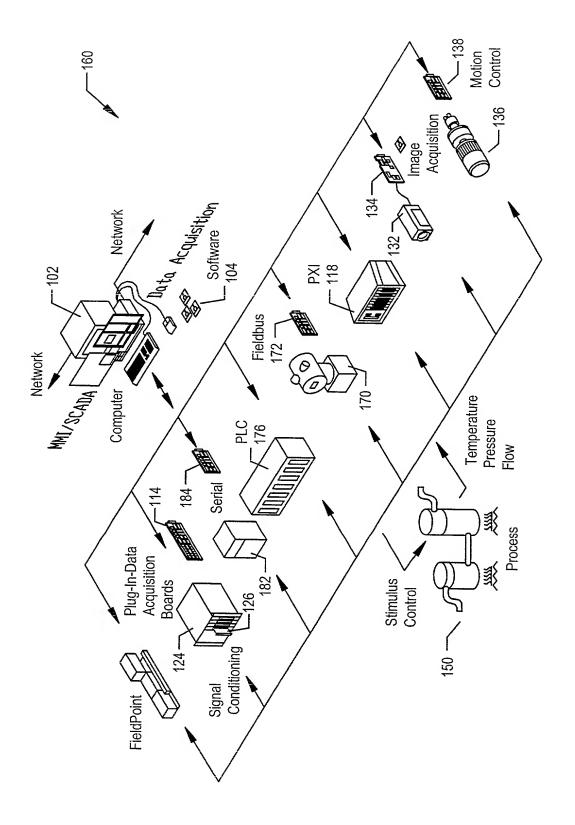


FIG. 1B

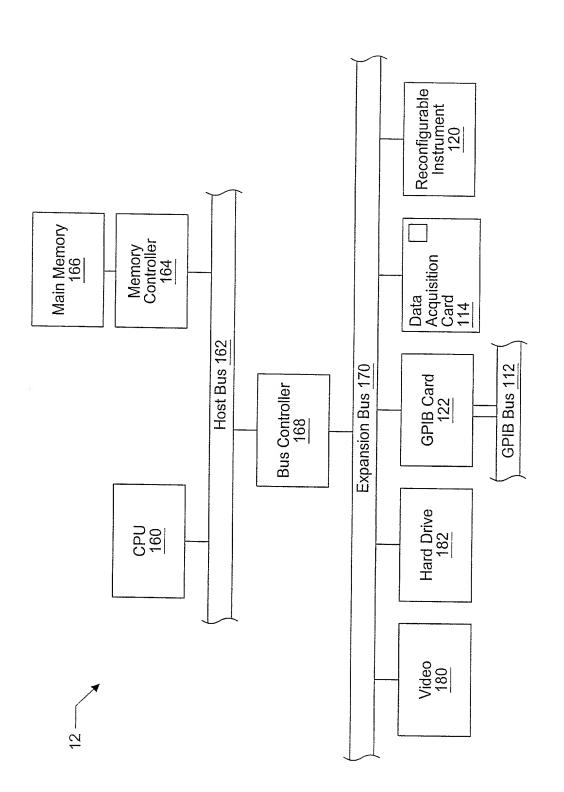


Figure 2

Figure 3

Figure 4

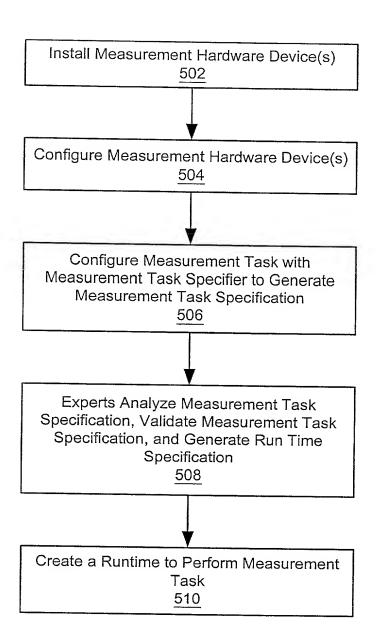
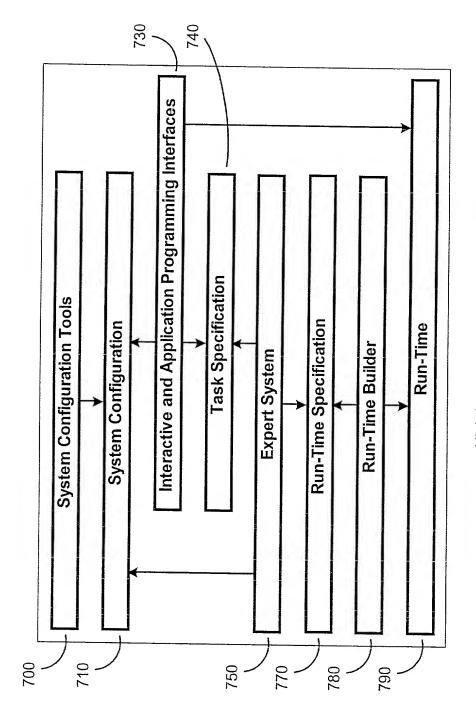


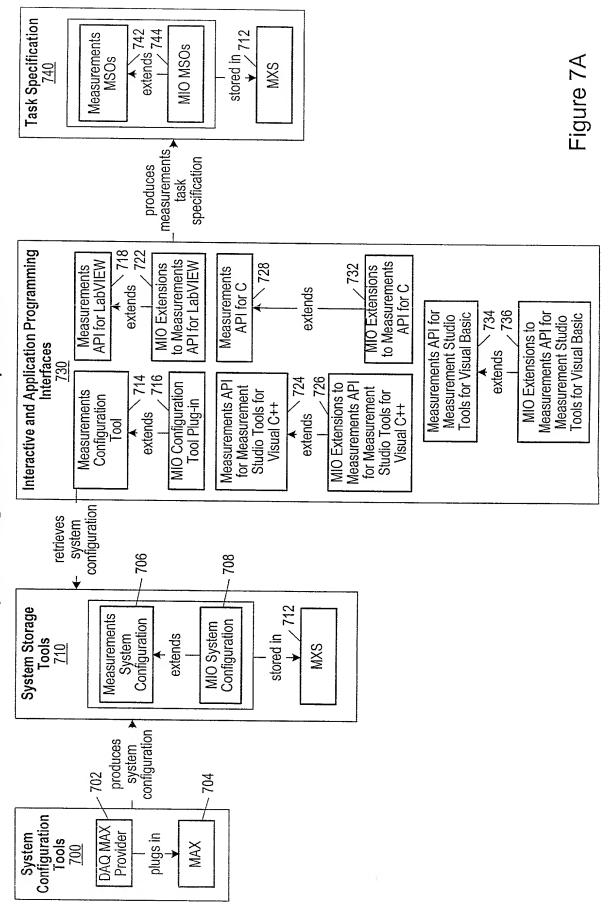
Figure 5

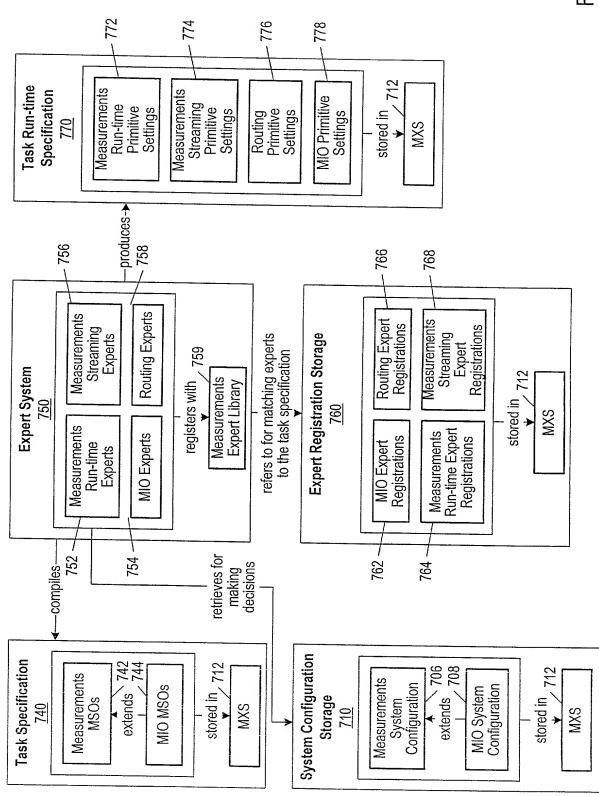


High-Level Architecture

Figure 6

System Configuration and Task Specification



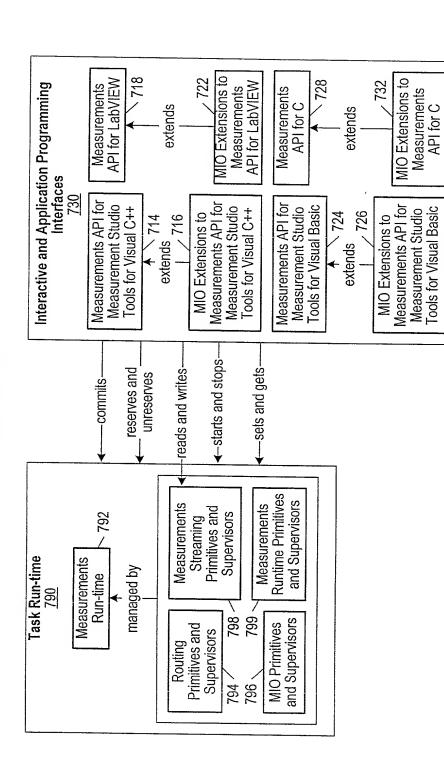


Compiling Task Specification to Task Run-time Specification

Measurements Run-time Primitives and Streaming Primitives and Supervisors Measurements 792 Supervisors Task Run-time Measurements managed by Run-time and Supervisors **MIO Primitives** Primitives and Supervisors Routing Building Task Run-time from Task Run-time Specification -spling-794 - 96/ - 788 789 Measurements Streaming Primitive Supervisors 782 Supervisors Routing Primitive creates primitive supervisors for each primitive settings Run-time Builder Measurements Run-time Builder Measurements MIO Primitive Supervisors Supervisors Run-time Primitive 786 retrieves-784 - 772 - 774 9// Task Run-time Specification <u>770</u> × 778 Measurements Measurements MIO Primitive Streaming Primitive Run-time Primitive Settings Routing Primitive Settings Settings Settings stored in MXS

- 798

- 799



Measurement Studio Tools for Visual Basic

MIO Extensions to Measurements API for

> 734 > 736

extends

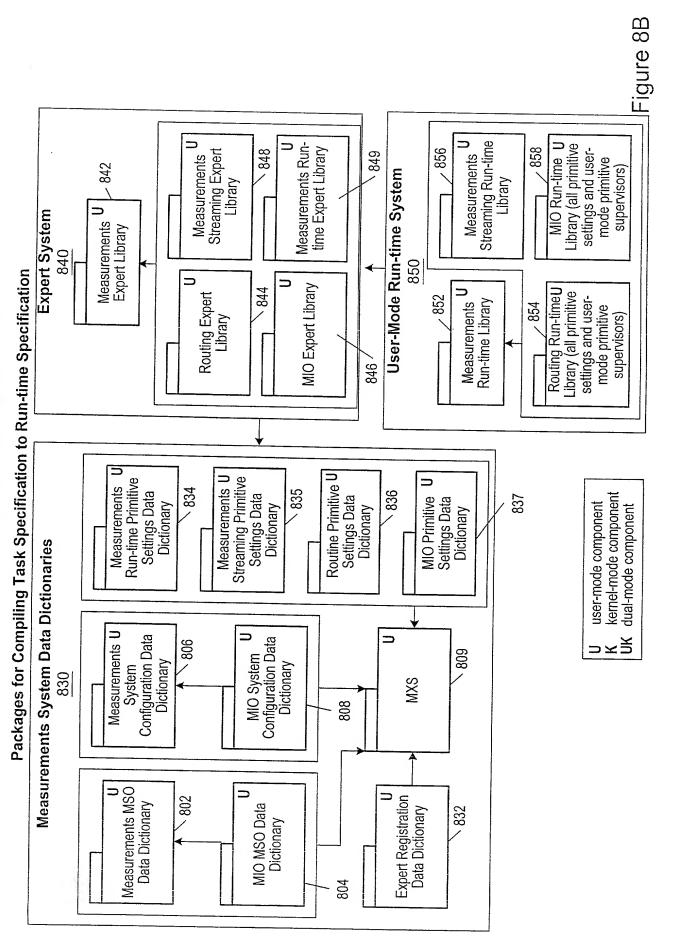
Measurements API for Measurement Studio

Tools for Visual Basic

Packages for System Configuration and Task Specification

for Measurement Studio Tools for Visual C++ Library Measurement Studio Tools for Visual C++ Library Measurements API U MIO Extensions to U Measurements API for for Measurement Studio Tools for Visual Studio Tools for Visual **824** 825 to Measurements API 828 829 Measurements API MIO Extensions for Measurement Basic Library Basic Library **API Libraries** MIO Extensions to Measurements API for LabVIEW Library Measurements API for LabVIEW Library MIO Extensions to Measurements API for C Library 822 823 Measurements API 826 827 for C Library System Configuration Data Dictionary Measurements U Configuration Data Dictionary 908 < MIO System 808 , 809 Data Dictionaries kernel-mode component 800 MXS user-mode component dual-mode component Measurements MSO 802 MIO MSO Data Dictionary Data Dictionary 804 \supset System Configuration DAQ MAX Provider 814 812 Tools 810 MAX

Figure 8A



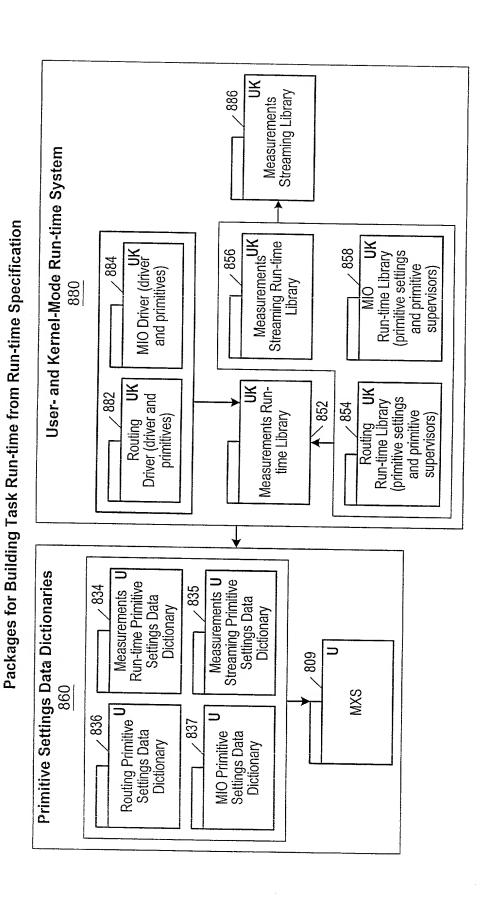
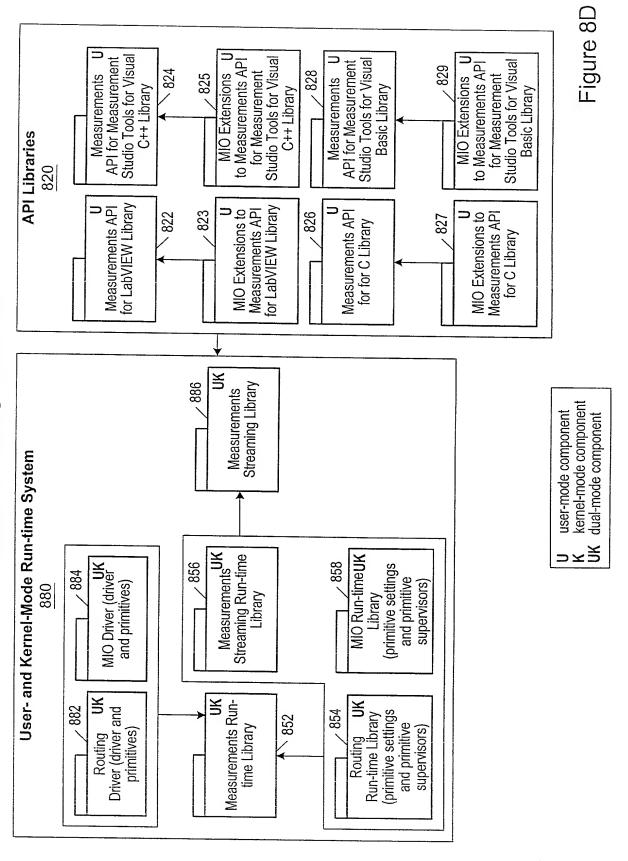
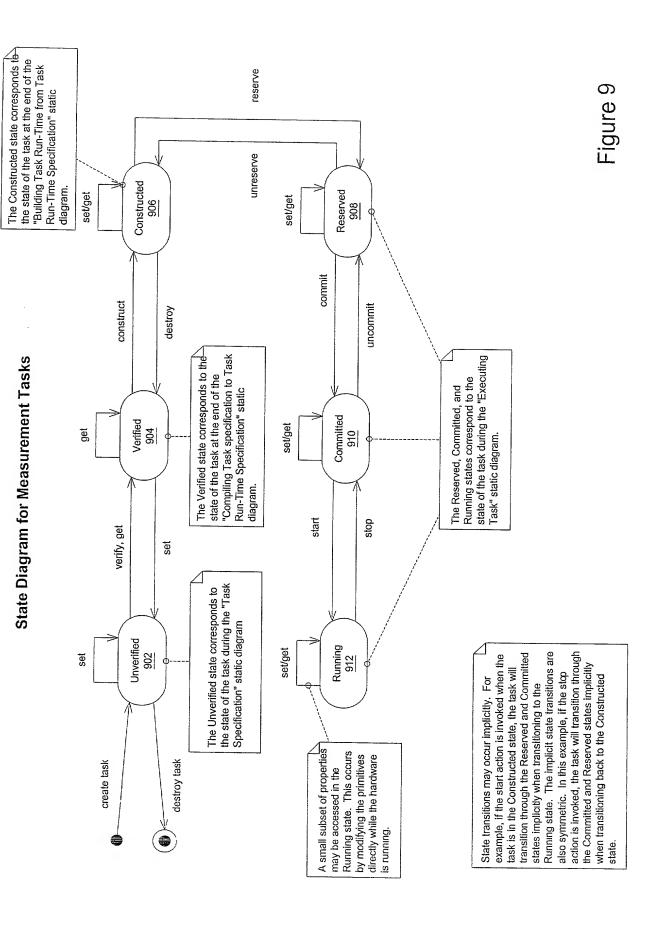


Figure 8C

kernel-mode component user-mode component dual-mode component

Packages for Executing Task Run-time





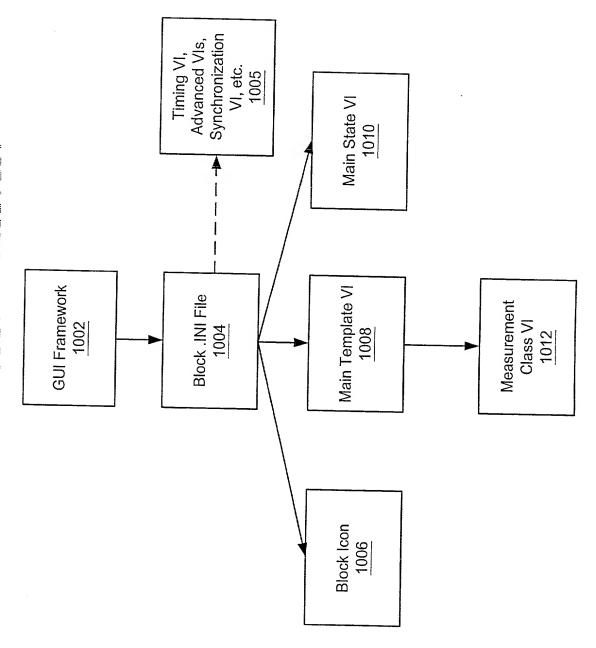


Figure 10

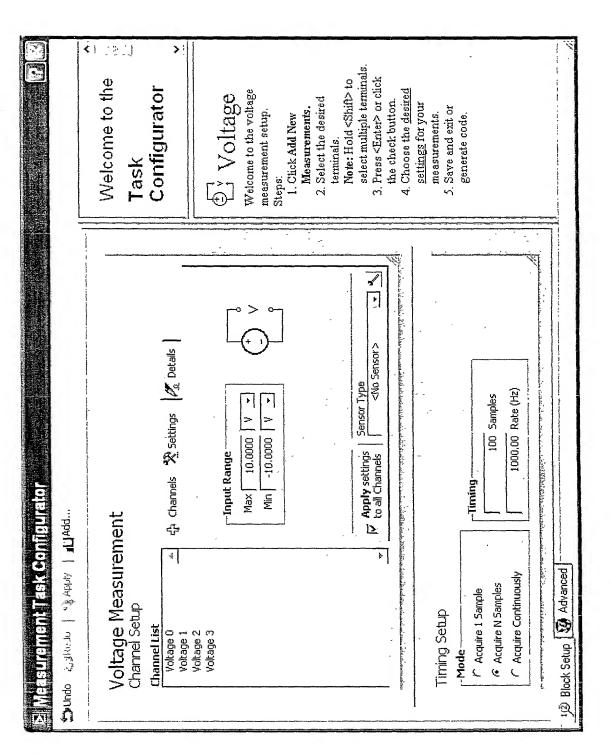


Figure 11

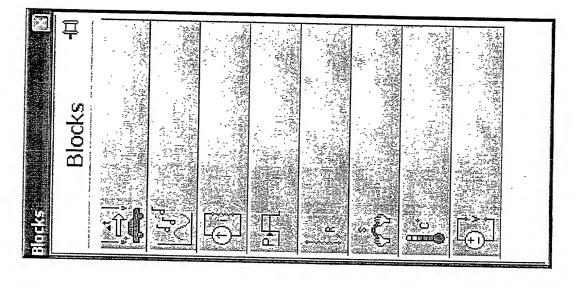


Figure 12A

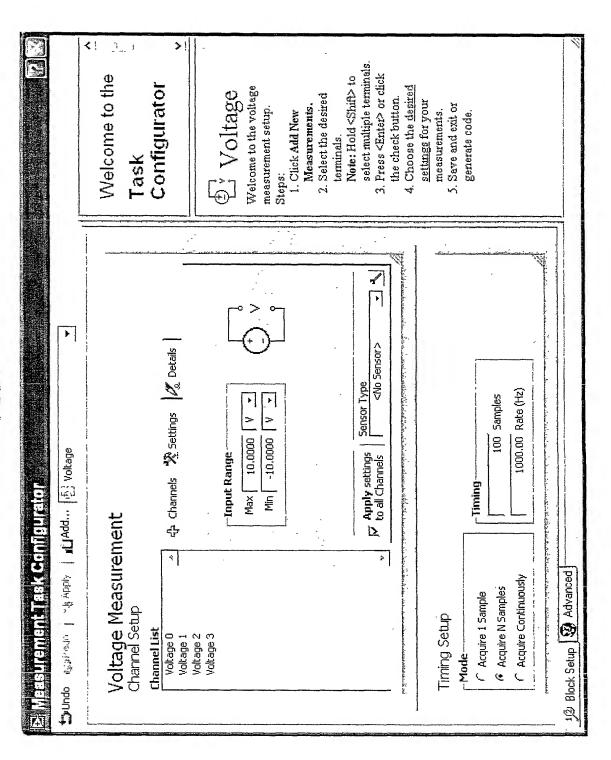


Figure 12B

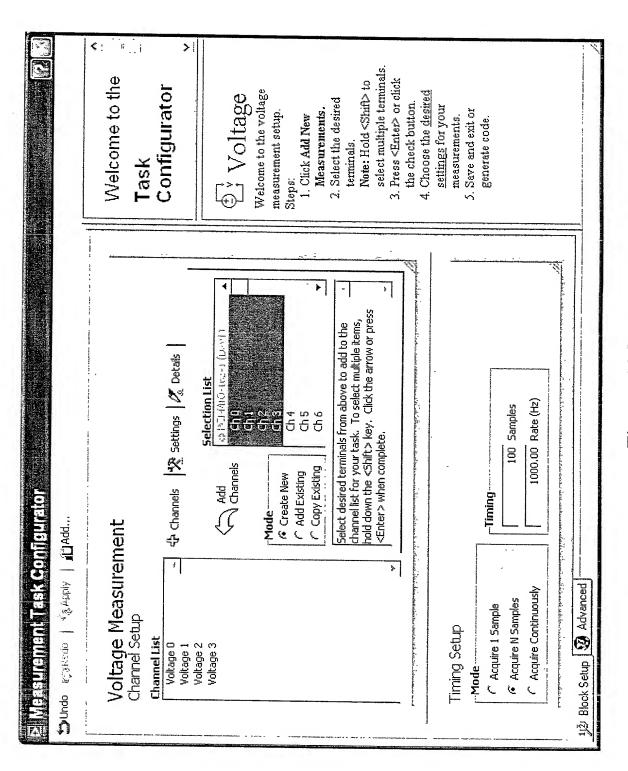


Figure 12C

	Welcome to the Task Configurator	o constant to the state of the						
III Measturement Task Configurator ≛>undo wastedo % Apply _i`DAdd	Voltage Measurement Channel Setup Channel List Voltage 0 Voltage 1 Voltage 1 Channels ★ Settings Channels ★ Details	Voltage 3 Custom Scaling Aloo Scale > Implement to the scale Imp	C Acquire Namples C Acquire Continuously					

Figure 12D

	Welcome to the Configurator Configurator Welcome to the voltage measurement setup. Steps: 1. Click Add New Measurements. 2. Select the desired terminals. Note: Hold <shift> to select multiple terminals. 3. Press <enter> or click the check button. 4. Choose the desired settings for your measurements. 5. Save and exit or generate code.</enter></shift>					
ElMeasurement Fask Configurator Dundo 《장무리 경제한 1DAdd	Format Data Type	S Rese	Trigger Type Source Slope Level Trigger Type PFI 0 1 Rising 0.0000	Source Edge Trigger Type Digital Edge Pre-Trigger Samples 0	Pause Prigger Type	12 Block Setup (A Advanced

Figure 13

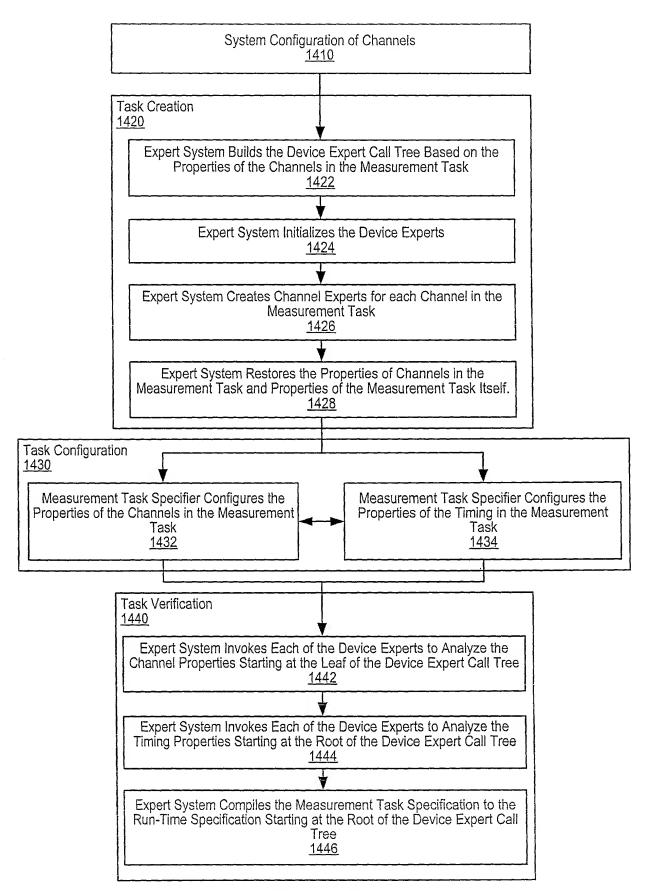


Figure 14

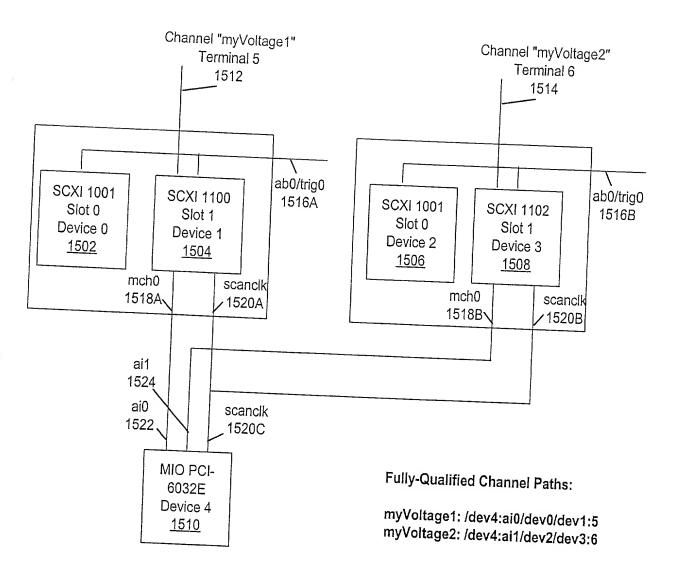
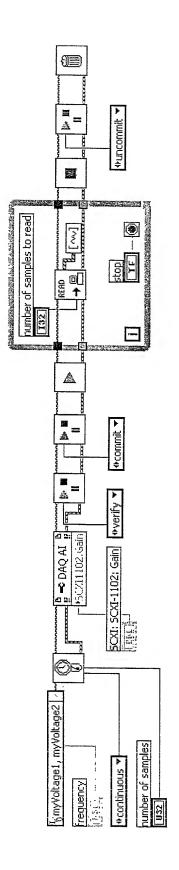
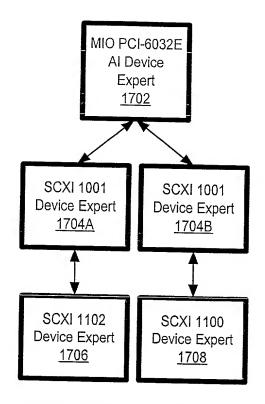


Figure 15



Voltage On Two Channels with Two SCXI Modules in Two SCXI Chassis Connected to an MIO DAQ Device

Figure 16



Create Device Expert Call Tree

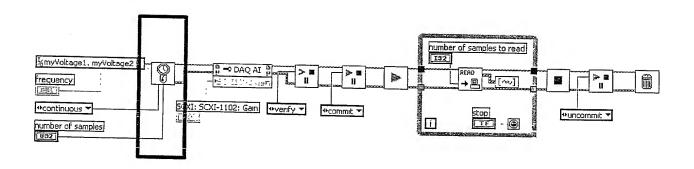
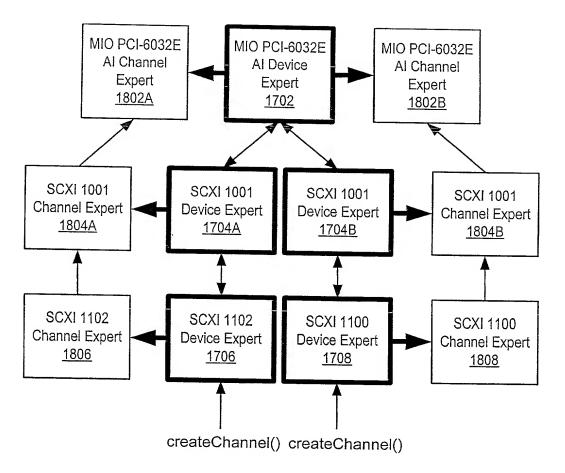


Figure 17



Create Channel Experts

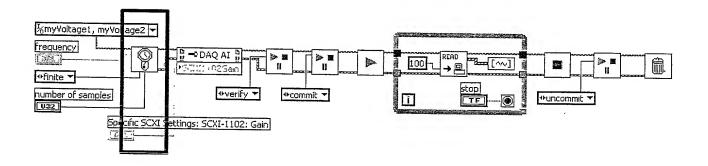
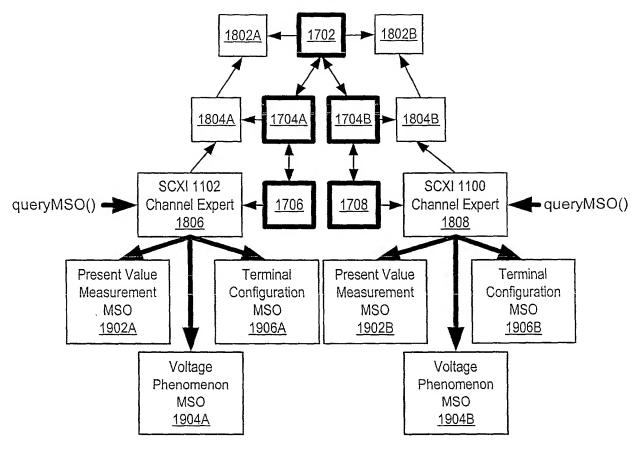


Figure 18



Deserialize Named Channel MSOs

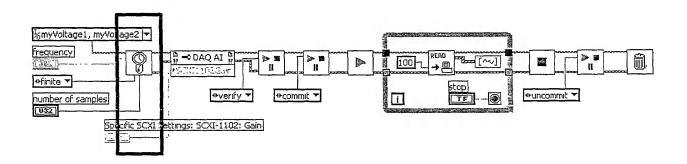
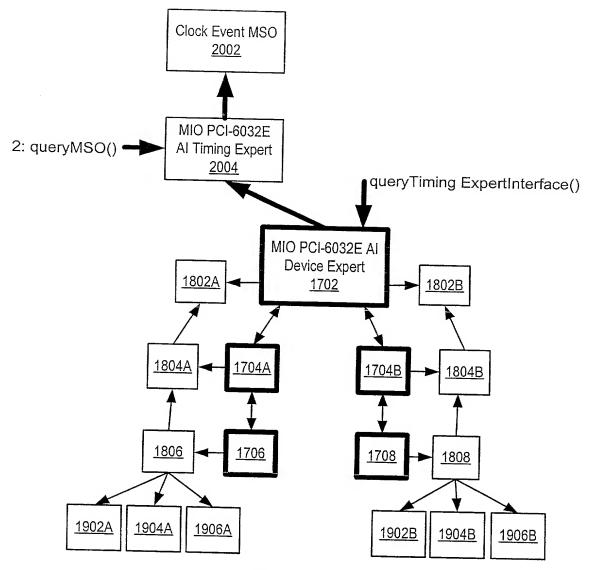


Figure 19



Configure Timing Experts

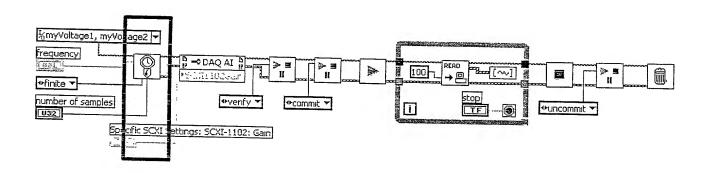
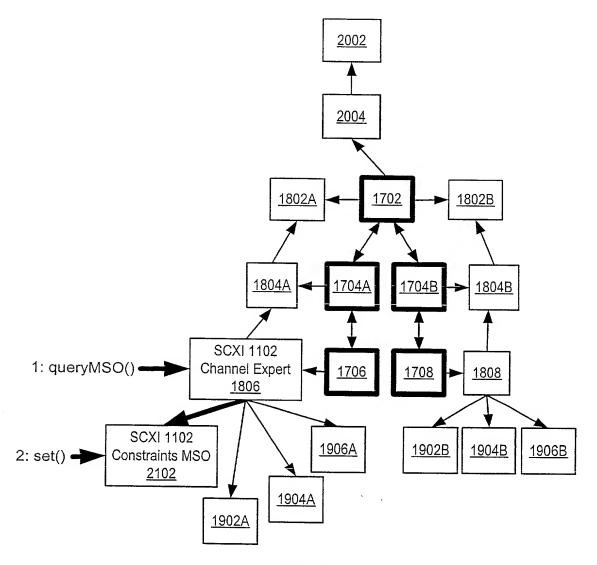


Figure 20



MSO Set Calls

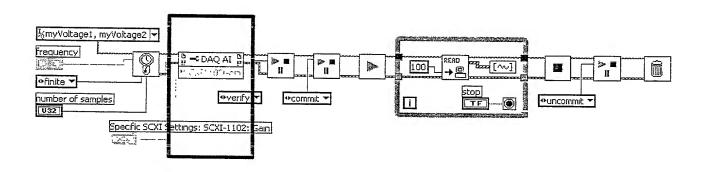
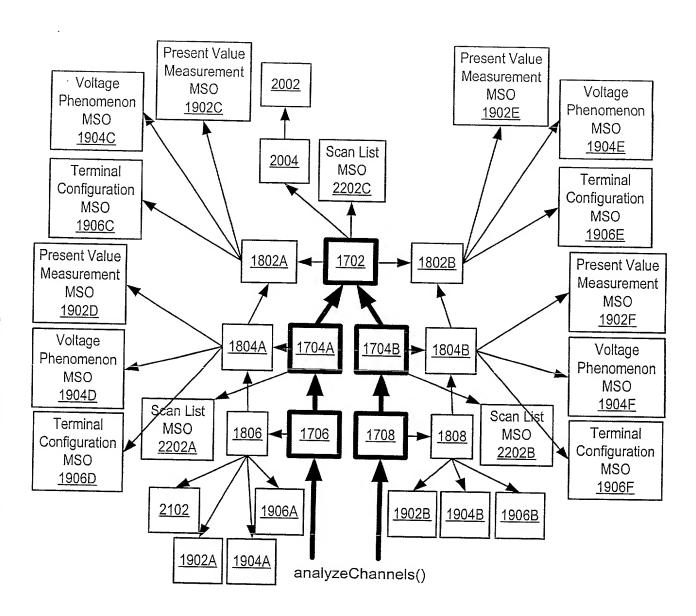


Figure 21



Analyze Channels

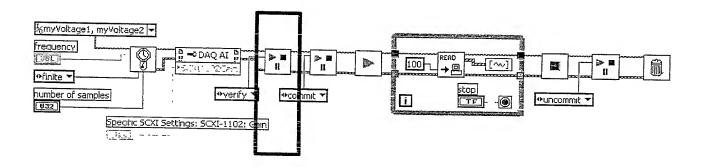
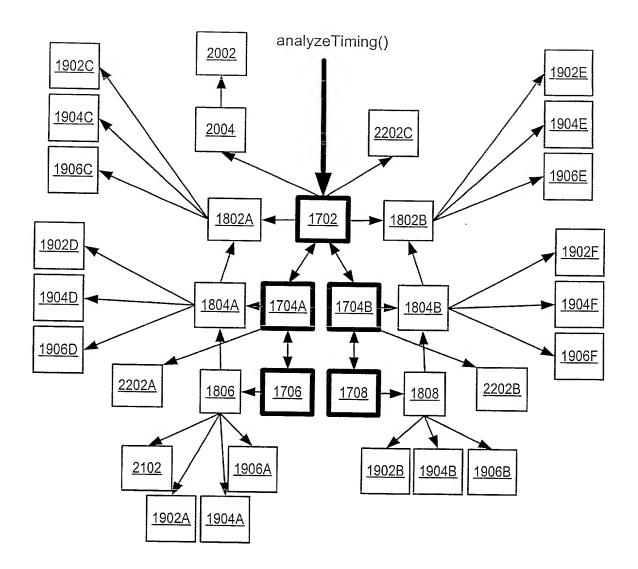


Figure 22



Analyze Timing

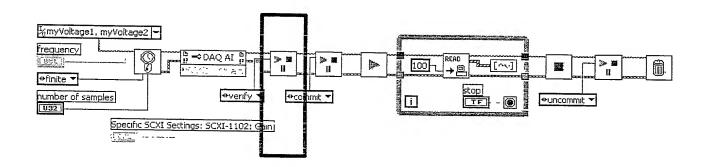
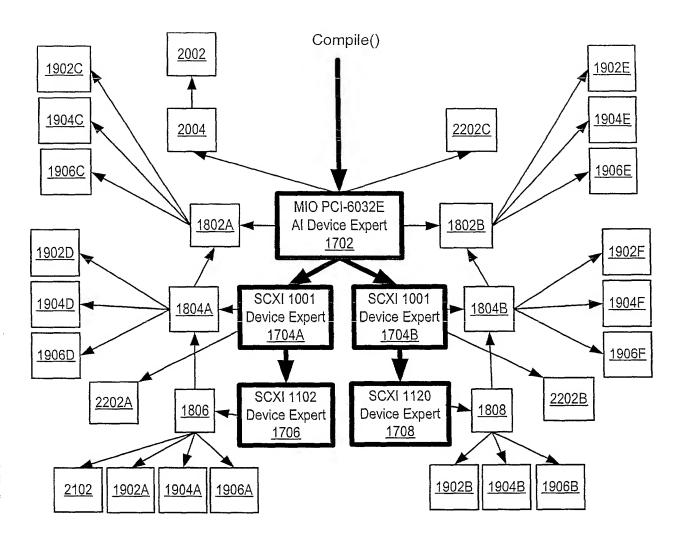


Figure 23



Compile

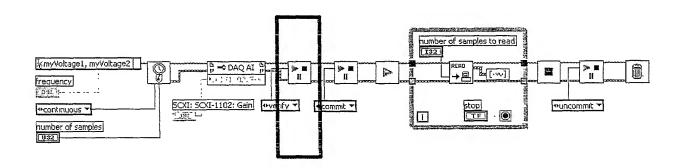
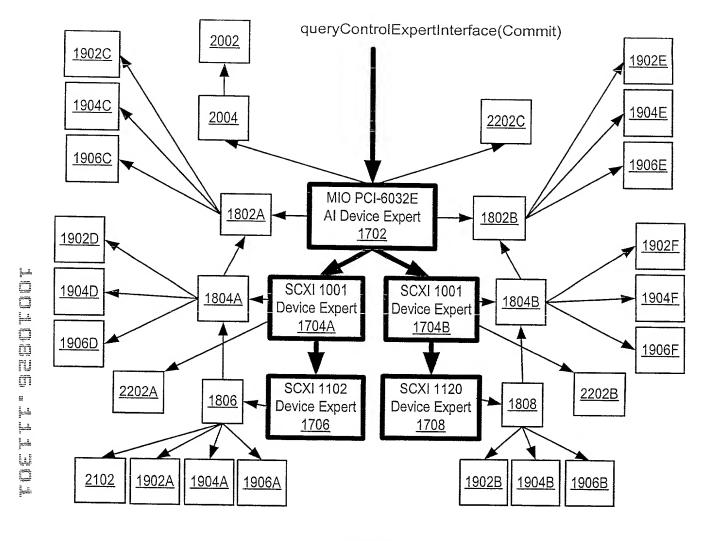


Figure 24A



Commit

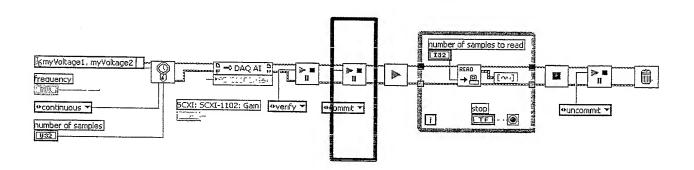
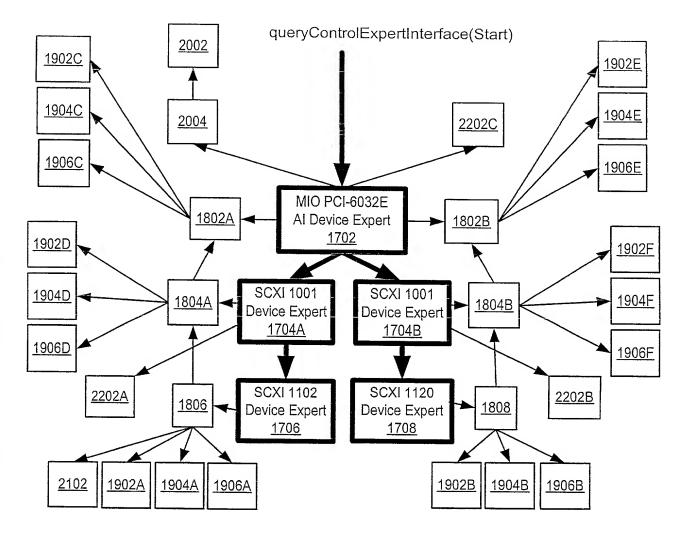


Figure 24B



Start

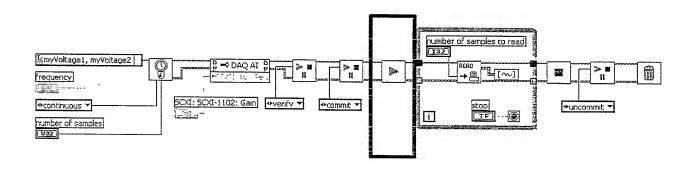
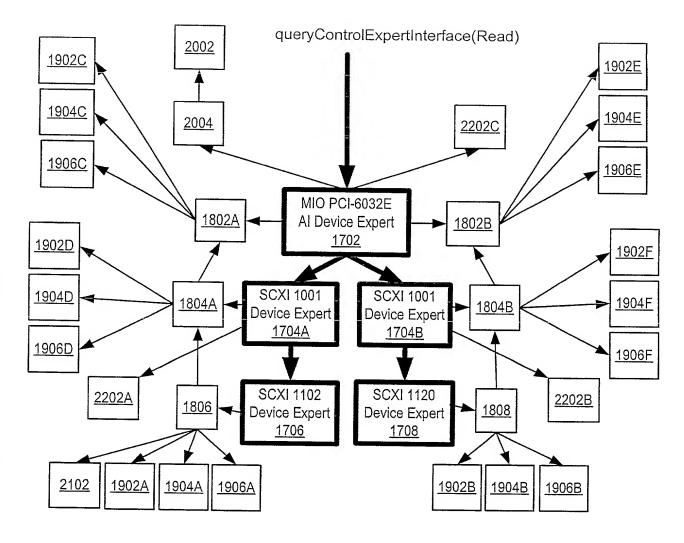


Figure 24C



Read

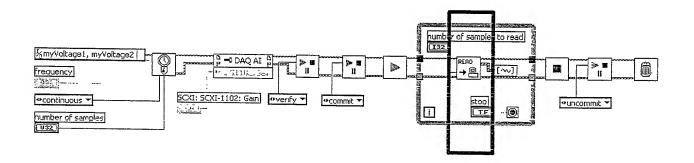
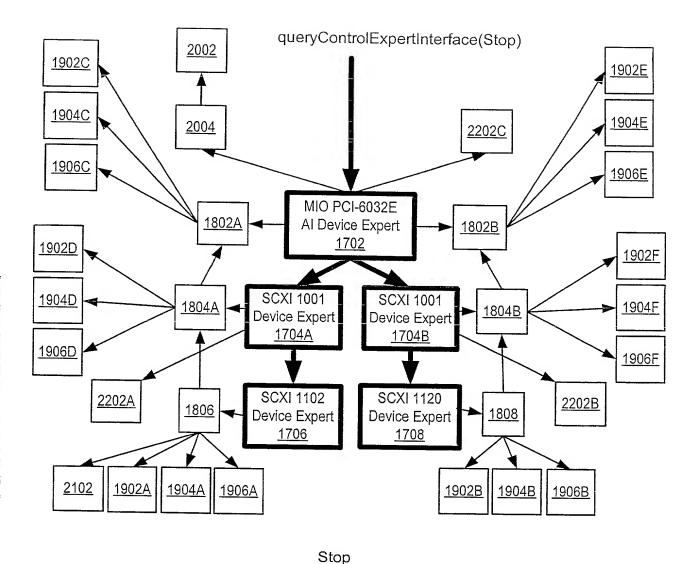


Figure 24D



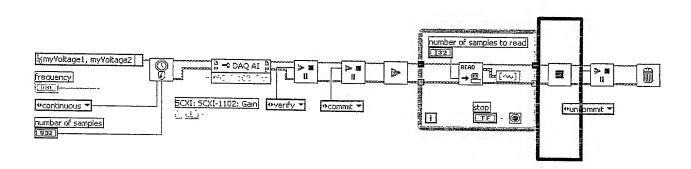
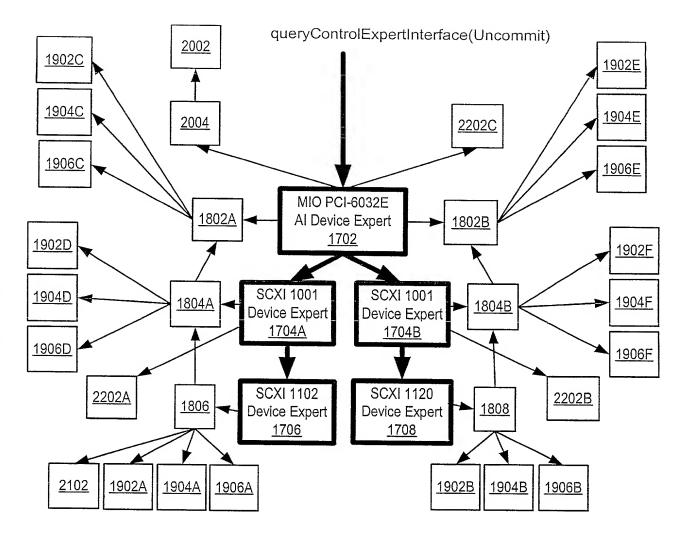


Figure 24E



Uncommit

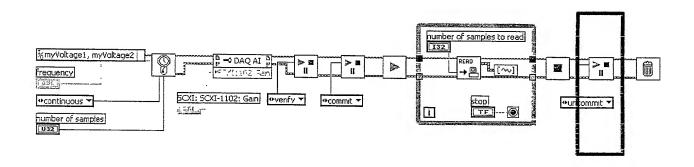
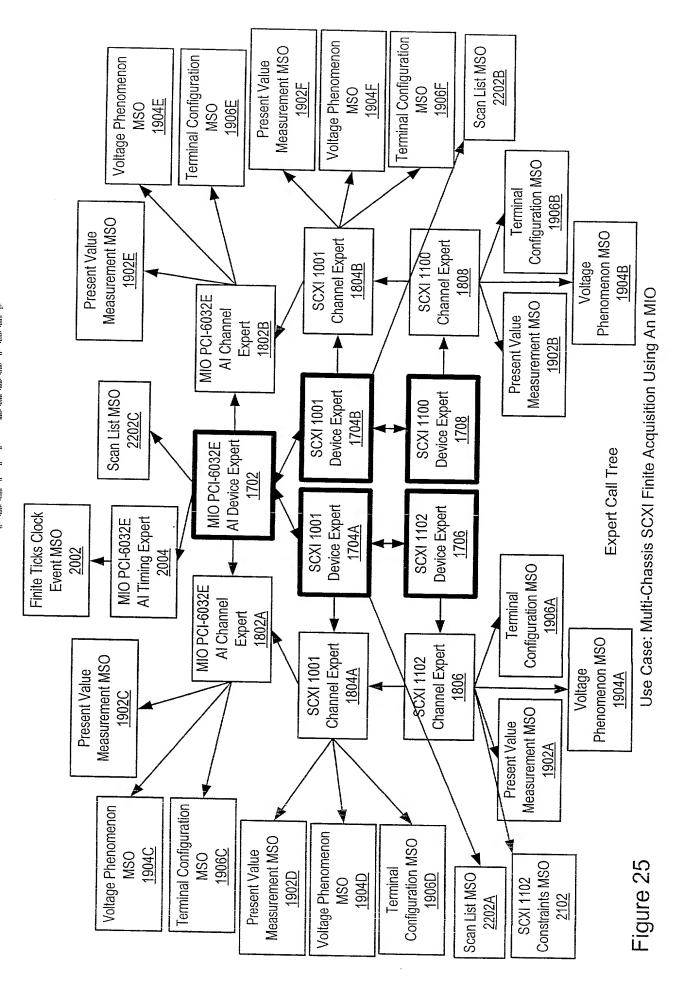


Figure 24F



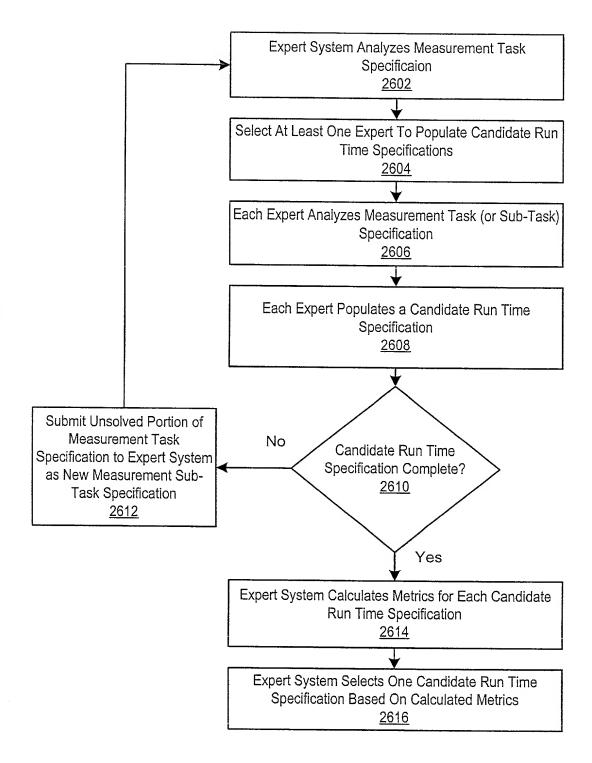


Figure 26

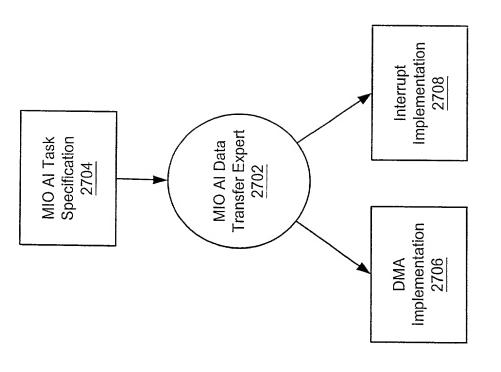


Figure 27

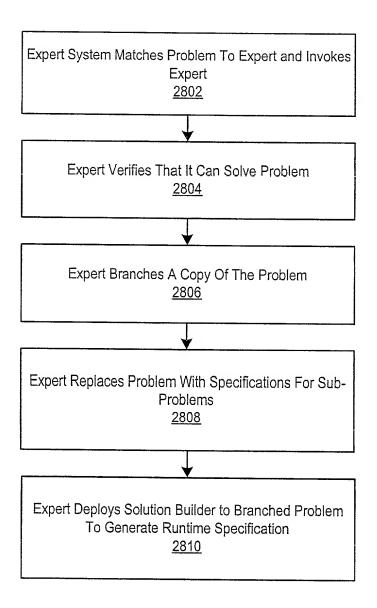


Figure 28

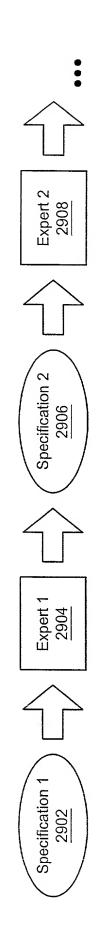


Figure 29

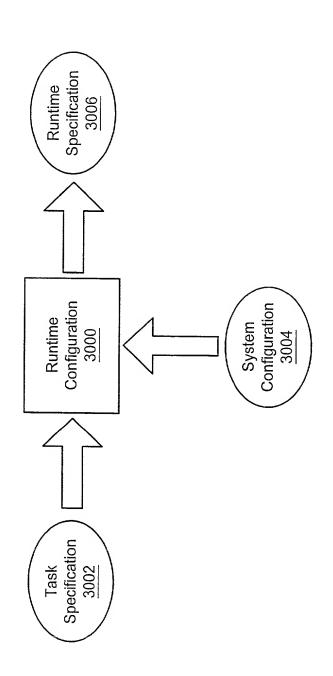


Figure 30

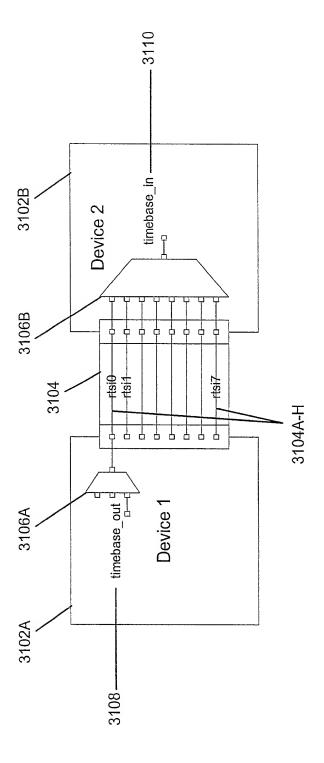


Figure 31

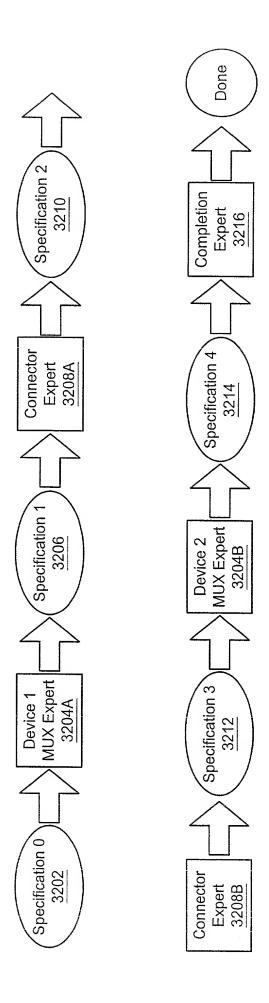


Figure 32

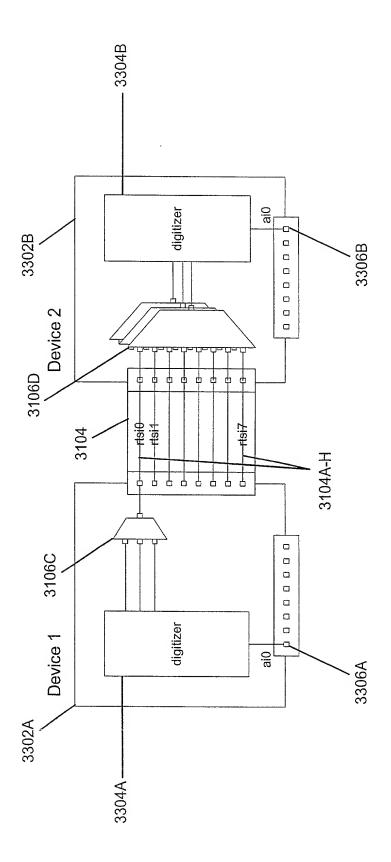


Figure 33

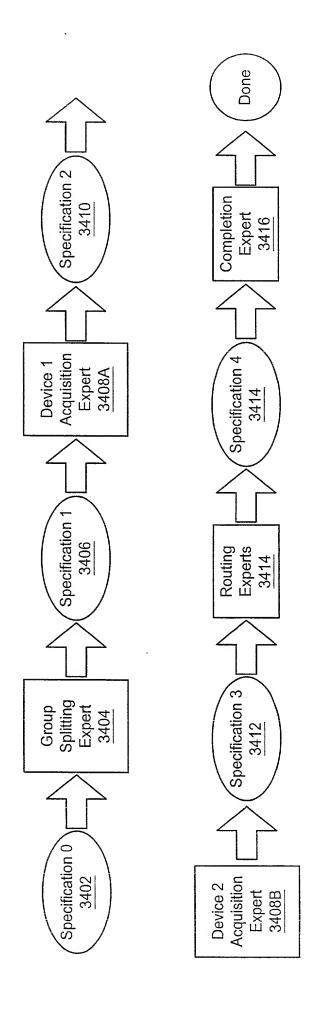


Figure 34

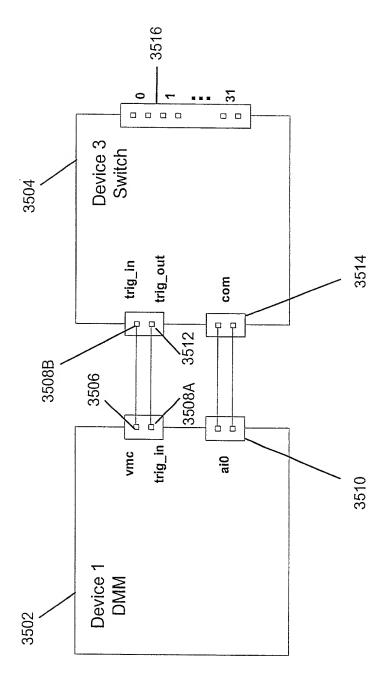


Figure 35

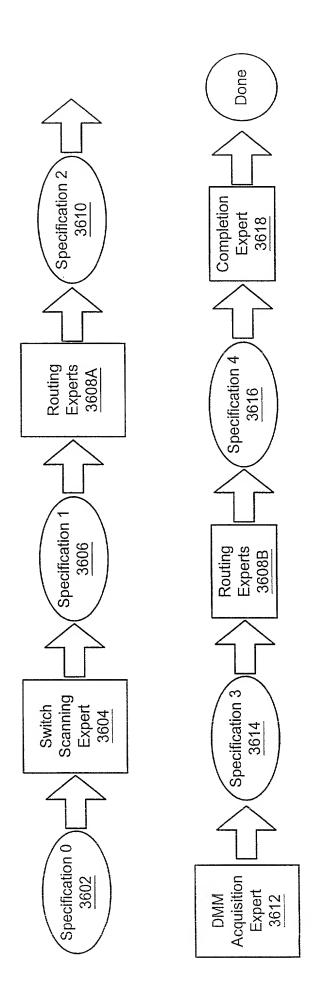


Figure 36

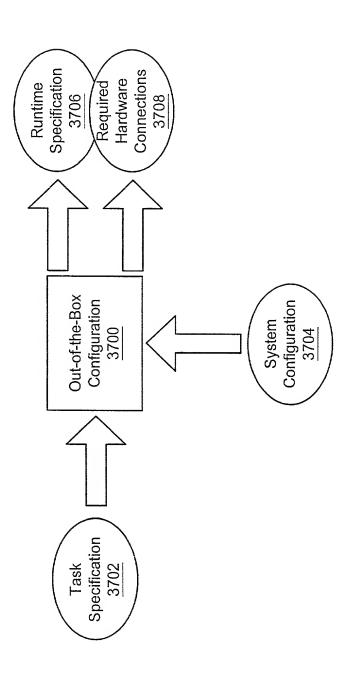


Figure 37

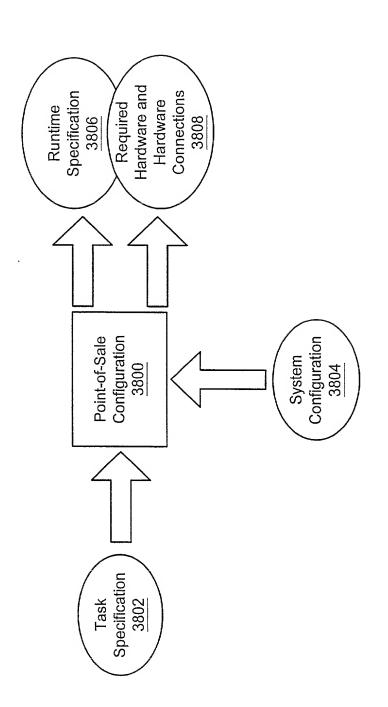
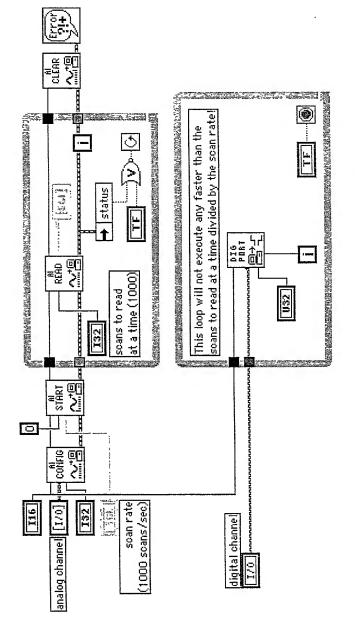
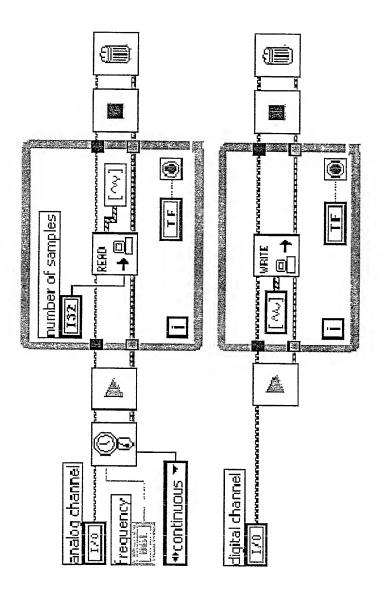


Figure 38



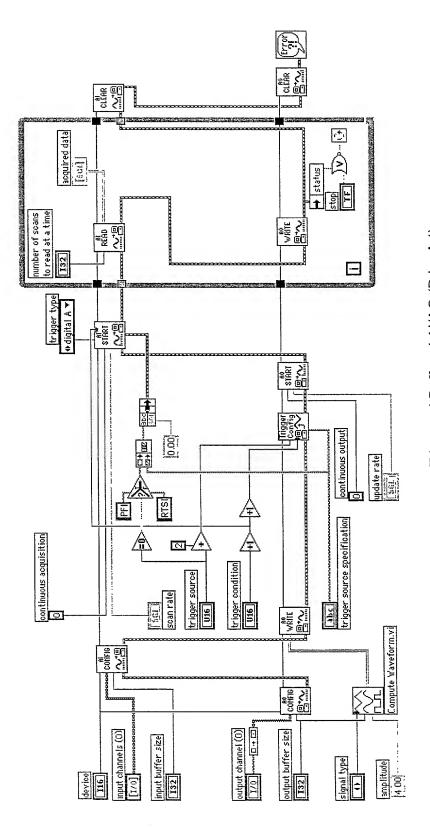
Simultaneous Buffered Analog Input And Single Point Digital Output With Single-Threaded Driver (Prior Art)

Figure 39A (Prior Art)



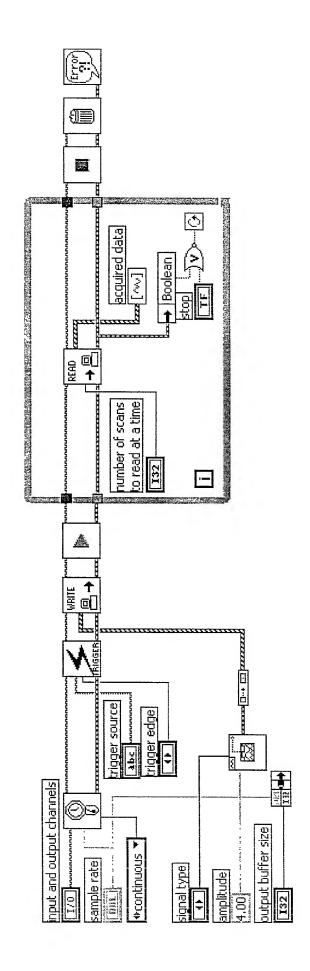
Simultaneous Buffered Analog Input And Single Point Digital Output With Multi-Threaded Driver

Figure 39B



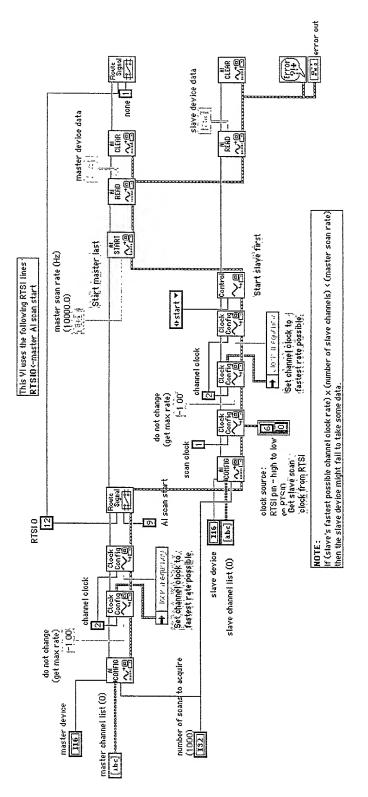
Simultaneous Triggered Buffered AI/AO (Prior Art)

Figure 40A



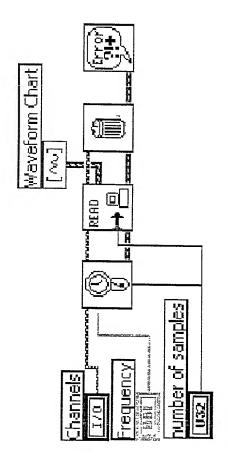
Simultaneous Triggered Buffered AI/AO

Figure 40B



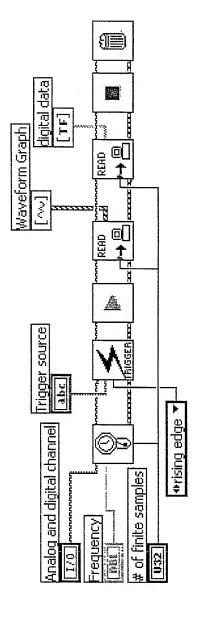
Sharing Scan Clock Across Two E-Series Devices (Prior Art)

Figure 41A



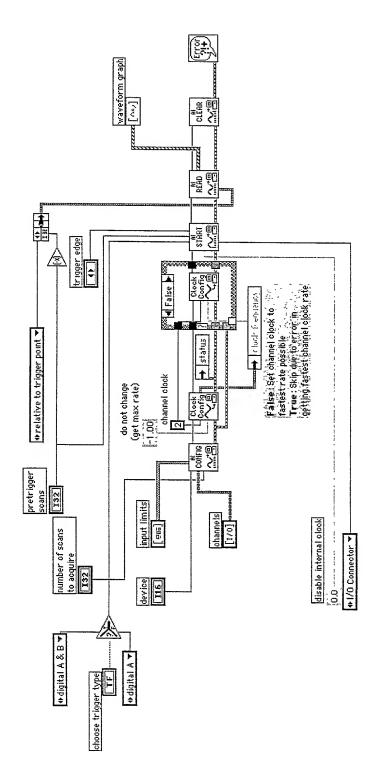
Sharing Scan Clock Across Two E-Series Devices

Figure 41B



Sharing Clock And Trigger, Buffered AI & DI

Figure 42



Acquire N Scans External Scan Clock Digital Trigger (Prior Art)

Figure 43A (Prior Art)

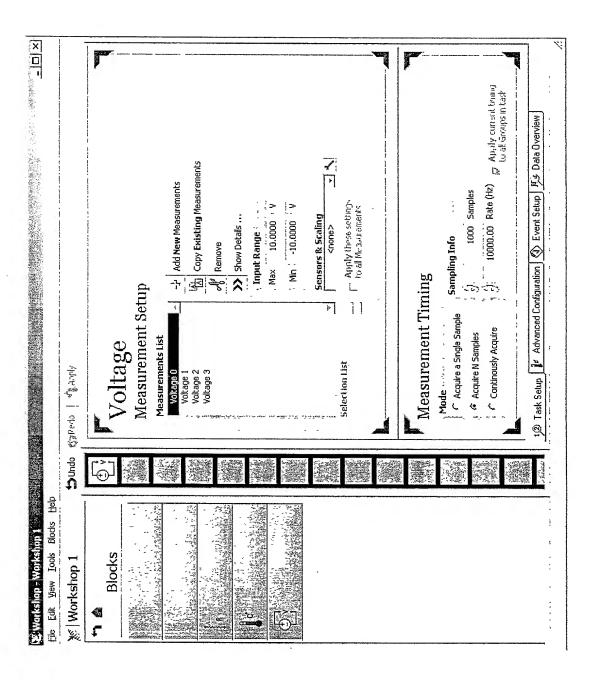
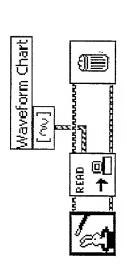


Figure 43B

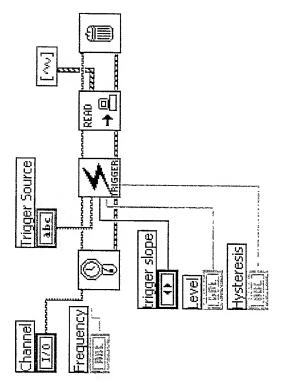
age allow you to twe no parameters for yo the mouse pointer over about what it doe Reset all to Defauli Type Scaled Anone > Chone >	File Edit Yiew Tools Blocks Help		
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Channel CDefault>		Advanced Mathematics	P
Measurement Triggering Measurement Triggering Trigger Type Source PFI_0 = Trigger Type Condition Source PFI_0 = Source PFI_0 = Trigger Type Condition Source FFI_0 = Source		rel Source	j
		1.00 Patr (Hz)	
		Measurement Triggering	
		Trigger Type Condition Digital Rising Edge	
0		Source PFI_0 -	
D D		Conduction None Software	
		Source SW	
		Trigger Type Canditra. Canditra.	
		Source 3W	

Figure 43C



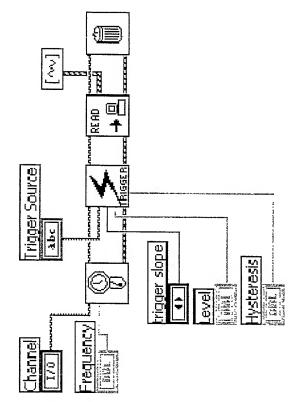
Acquire N Scans External Scan Clock Digital Trigger

Figure 43D



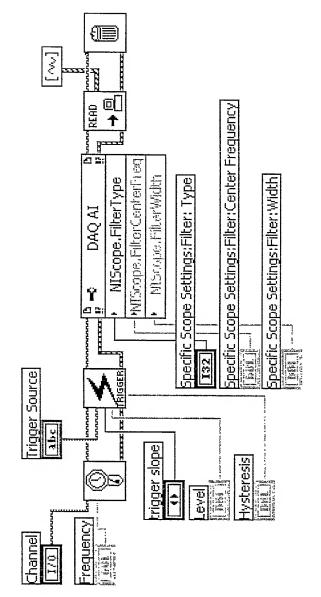
Triggered Acquisition With E-Series Device

Figure 44A



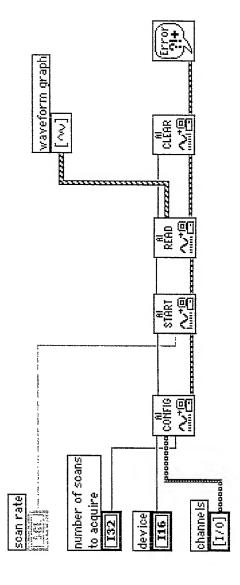
Triggered Acquisition With High Speed Digitizer

Figure 44B



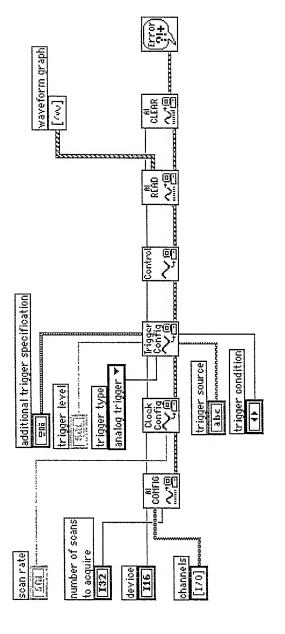
Triggered Acquisition With High Speed Digitizer With Filtering

Figure 44C



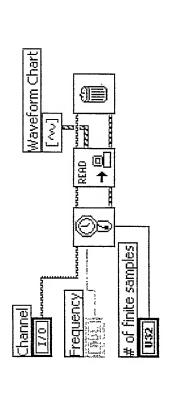
Intermediate Layer (Prior Art)

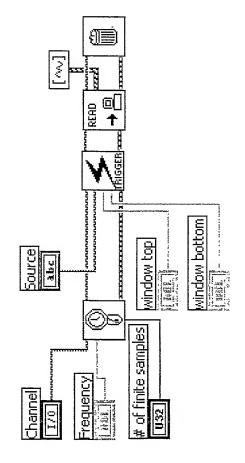
Figure 45A



Changes For Analog Window Triggering (Prior Art)

Figure 45B





Analog Window Triggering

Figure 45C